
Response Under 37 CFR § 1.116
Expedited Procedure - Group 2823

03500.109594.

PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:)
DAISUKE MIURA, et al.) : Examiner: Julio J. Maldonado
Application No.: 10/555,614) : Group Art Unit: 2823
371(c) Date: November 4, 2005) :
For: FIELD EFFECT TRANSISTOR,)
METHOD OF PRODUCING) :
THE SAME, AND METHOD) :
OF PRODUCING LAMINATED) :
MEMBER) : November 10, 2008

Mail Stop AF
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

OK TO ENTER: /J.M./

AMENDMENT AFTER FINAL REJECTION

Sir:

In response to the final Office Action dated September 3, 2008, please
amend the above-identified application, as follows: